

What the Buzz is All About...

The Low Power Reference Platform Exclusively from Arrow

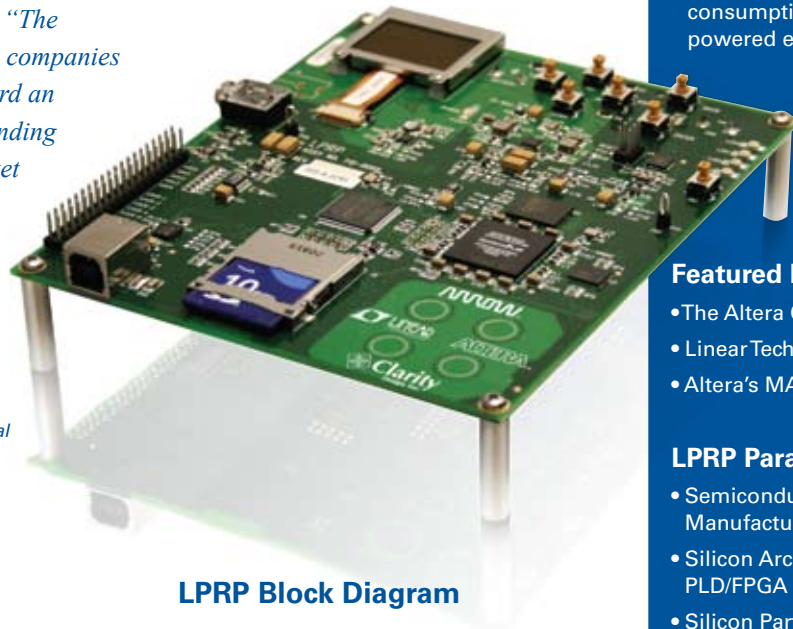
Arrow Electronics, Altera Corporation, and LinearTechnology have combined industry-leading power management solutions in the revolutionary LPRP. Purchasing an LPRP kit enables designers to learn first-hand the latest design techniques for power-sensitive

applications, from battery charging to understanding the interaction between device selection, system performance, system power modes, and their combined effect on power consumption.

Along with the LPRP tool... *"The collaboration between these companies makes this development board an interesting case study in blending objectives of changing market perceptions, selling chips, and providing a robust tool to facilitate product prototyping and development."*

Kevin Morris
Editor
FPGA and Structured ASIC Journal

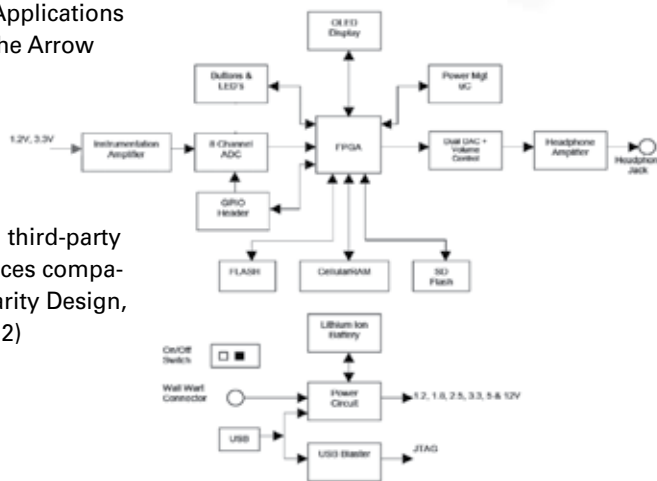
Quoted from "Battery-Powered Proof - Development Board with a Mission."



Design Note

Steven Kravatsky, Field Applications Engineer at Arrow, and the Arrow Consulting Engineering Services (ACES) team were the brains behind the Low Power Reference Platform, with help from two of Arrow's third-party engineering design services companies, Xaplos, Inc. and Clarity Design, Inc. (see ACES on page 52)

LPRP Block Diagram



FREE LPRP Webcast Low Power Reference Design Platform Webcast

See how a low-power FPGA system can become a reality and learn how Altera's Cyclone III FPGA, with its unique power-optimized process and architecture, can be used in conjunction with power solutions from Linear Technology. Check out real-world solutions for FPGAs in ultra-low-power

applications. Brought to you by Arrow Electronics in partnership with Altera and LinearTechnology.

Launch the Low Power Reference Design Platform webcast available at: www.arrow.com/atsf

LPRP Represents a Complete Arrow Solution:

- Part number: LPRP
- Takes advantage of low-power Altera Cyclone III FPGAs
- Uses highly efficient power conversion solutions from LinearTechnology
- Utilizes power from and includes charging of Lithium Ion batteries
- Demonstrates how to minimize power consumption in portable and battery-powered embedded systems
- Gives designers flexibility to create application-specific low-power solutions

Featured Devices:

- The Altera Cyclone III EP3C25
- LinearTechnology's LT3455
- Altera's MAX IIG CPLD (EPM240T100)

LPRP Parametrics:

- Semiconductor Manufacturer: Altera
- Silicon Architecture: PLD/FPGA
- Silicon Part Family: Cyclone III
- Device: EP3C25

The LPRP Kit Includes:

- An LPRP development board (functions as MP3 player and picture viewer)
- A detailed LPRP User Guide and reference materials on SD card with reader
- Quartus II and the Nios II EDS software (required to use and/or modify the designs used with this kit)
- A Quick Start guide
- One pair of headphones
- A USB cable

To purchase the LPRP, or if you would like hands-on training, go to www.arrow.com/lprp, or call your Arrow representative at 877-237-8621.